ASIC / SoC
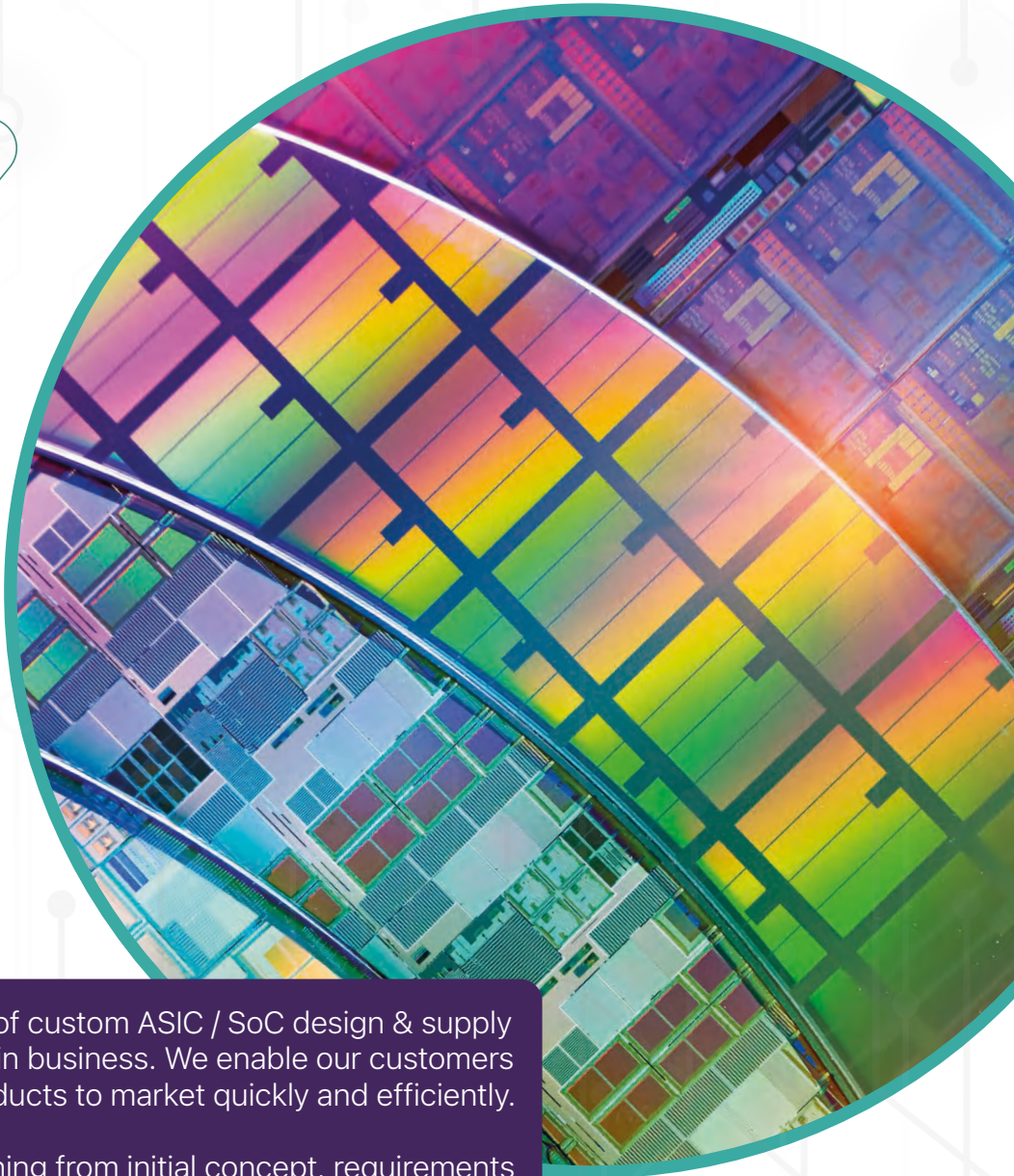## design & supply services

easics is a leading provider of custom ASIC / SoC design & supply services with over 30 years in business. We enable our customers to bring their innovative products to market quickly and efficiently.

Our services include everything from initial concept, requirements engineering, chip architecture, IP selection, design & verification, to FPGA prototyping, testchip validation, characterization, manufacturing & test.

easics is an ISO 9001:2015 certified company.

easics
get our logic in your system

www.easics.com

your trustworthy ASIC / SoC design partner

long-term partner
- support throughout your product lifecycle
- many 10+ years customers

risk reduction
- pre-project risk register and verification levels ensure on-time product launch
- starting from scratch or from customer legacy

effective communication
- we will update you on project progress, and promptly address any issues

ownership & accountability
- taking responsibility for quality, schedule and budget

product families
- we can design scalable / future-proof blocks to enable a product family of chips

IP security
- our top-notch IT infrastructure protects your IP

our core business

digital ASIC / SoC design & verification

macro & full chip: RTL to GDSII

VHDL, Verilog, SystemVerilog, UVM, SystemC

PPA optimization

wreal, Verilog-AMS, UVM-AMS

ULP: clock gating, power gating

production test

beyond ASIC / SoC design
a full spectrum of services

- **concept, business case, feasibility**

- **requirements engineering & architecture**

- **system modeling, algorithms & DSP**
  SystemC, Python, Julia, Matlab, C++, C, virtual platforms

- **embedded software development**
  for RISC-V, ARM and other processors

- **FPGA SoM / PCB design for ASIC proto-typing, lab validation and evaluation kit:** utilizing easics' 10 Gbit/sec or 1 Gbit/sec TCP/IP acceleration IP cores over Ethernet, or PCI-x plug-in cards

- **lab validation / measurements**
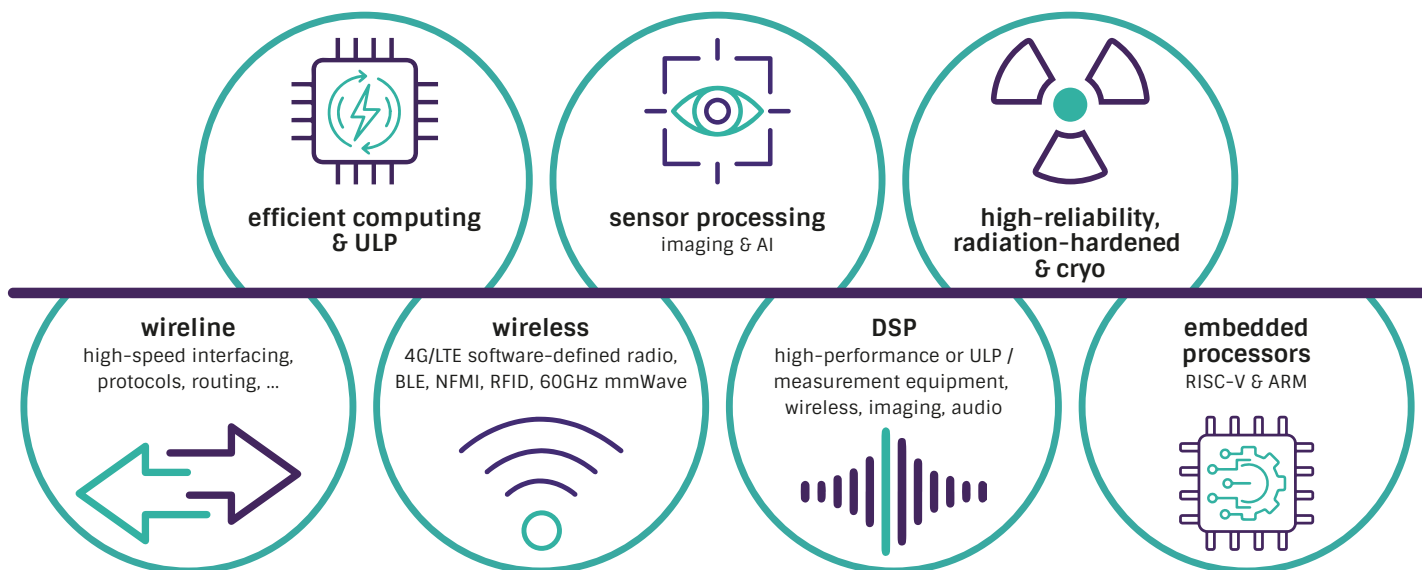 automated setups
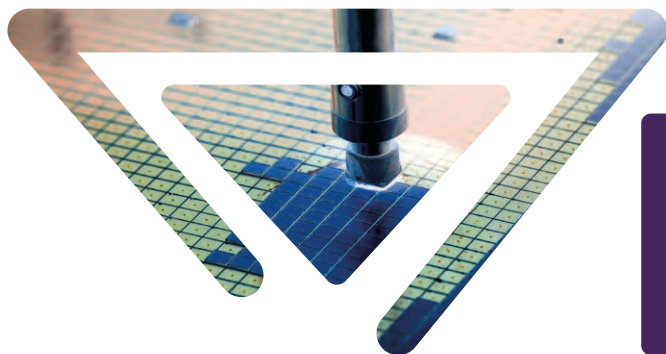
- **design & verification methodology**

- **design services for our customer's customers (CC)**
  integration of customer's chip, designed by easics, into CC's product

first-time right

our expertise

efficient computing & ULP

sensor processing
imaging & AI

high-reliability, radiation-hardened & cryo

wireline
high-speed interfacing, protocols, routing, ...

wireless
4G/LTE software-defined radio, BLE, NFMI, RFID, 60GHz mmWave

DSP
high-performance or ULP / measurement equipment, wireless, imaging, audio

embedded processors
RISC-V & ARM

technology nodes

180nm | 65nm - 22nm | 22nm FD-SOI | FinFET 16nm, 12nm & beyond

we are an independent design house

▷ supporting a *wide* range of foundries

▷ *both* Cadence and Synopsys EDA flows are supported

development process

1 design proposal

2 requirements & architecture

3 design & verification

4 chip-level integration

5 tape-out / manufacturing, test & supply

ASIC / SoC design & supply services

chip solutions for highly demanding markets

▷ easics develops custom ASICs / SoCs tailored to the customer needs and market requirements.

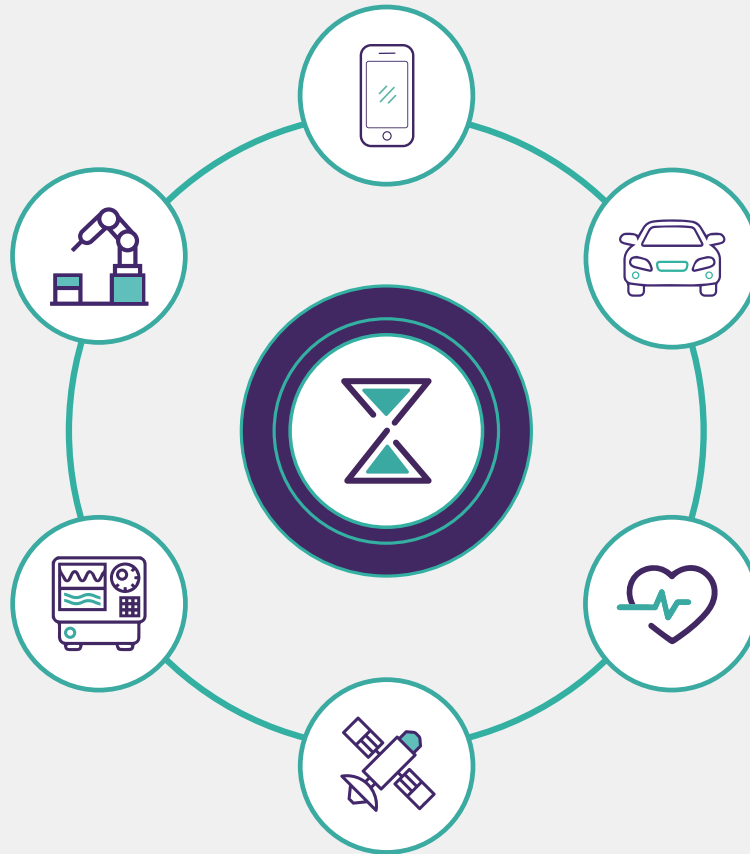▷ we keep up-to-date with the latest semiconductor technology trends and chip design & verification methodologies.

IoT / mobile / consumer
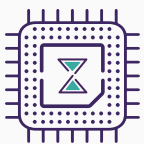
industrial

measurement equipment
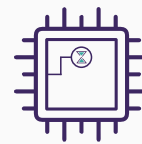
automotive

healthcare

space & other markets

we also offer:

FPGA / SoC design services

AI IP cores
ULP near sensor
nearbAI™

TCP/IP acceleration IP cores

easics is market leader in ASIC / SoC design & supply services. easics also offers FPGA / SoC-based designs and develops and licenses nearbAI™, our semiconductor IP cores for ultra-low power near-sensor AI in FPGA and ASIC.

ISO 9001 2015 **CERTIFIED**

30 YEARS IN BUSINESS
founded 1991

ASIC - V1.4

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