

SoC / ASIC DESIGN ENGINEER

Location: LEUVEN – BELGIUM

easics is permanently looking for colleagues to surf the deep tech wave

You want to contribute on the frontline of the digital revolution?

Great!

You want to design state-of-the-art digital chips that will become the cornerstones of next-generation products in Ultra-Low Power mobile & consumer, healthcare, automotive, and other advanced applications such as smart Augmented Reality (AR) glasses?

Perfect!

You have a Master of Science or PhD degree in Electronics Engineering, Computer Engineering/Science, Physics, Applied Mathematics, or similar?

Terrific! Read on!

We make the world a smarter place

easics is a worldwide reference in smart electronic System-on-Chip (SoC) design. Our customers are world-leaders in their respective markets. We tackle some of the most advanced and rewarding challenges in first-of-a-kind digital & mixed-signal chips. Take for example: live, Ultra-Low Power pattern recognition using neural network inference at the extreme edge, top-notch hearing implants helping newborns and grandmas alike, a radiation-hardened camera for earth observation satellites, energy-harvesting wireless tags, ...

We discuss state-of-the-art algorithms and requirements with our customers and turn them into cutting-edge reality, tackling the challenges inherent to deep submicron implementations, all the way to 4-nanometer. We model these hardware challenges in software and optimize over all layers of abstraction. Trade-offs in power consumption, performance, and area are made along the way, resulting in the physical realization: a SoC or a custom-tailored ASIC at the heart of next-generation innovative products.

We'll make you an expert, no worries

Whether you are an eager starter or a seasoned professional, we'll guide you through our design methodology and introduce you to our vast body of expertise. easics is known to be a learning company; one that will give you the opportunity and time to become an expert, no matter what your previous experience is. You will join the easics academy from day one.

You will work on projects in diverse domains, making high-level models, mastering the intricacies of timing and parallelism, running tests in our lab or discussing requirements and architecture with the customer. You'll work in a highly autonomous team, where everyone is always ready to provide helping hands and brains to the others.

WE CAN'T WAIT TO SEE YOU JOIN OUR GROWING TEAM

See next page for more details



SoC / ASIC DESIGN ENGINEER FULL-TIME

Specializing in digital SoC, ASIC, FPGA, and DSP design

You will model, design and verify Ultra-Low Power and High-Performance digital Systems-on-Chip (SoCs), custom ASICs and FPGAs. You will use VHDL, (System)Verilog, UVM, and Python; next to SystemC/C++ and Linux. You will enjoy using easics' state-of-the-art chip methodology and tools, complemented with industry-leading Electronic Design Automation (EDA) tools. You will work closely with easics' international tier-one customers, partners and subcontractors.

DO APPLY IF:

- ▶ you have a keen interest in digital SoC design and software methodology
- ▶ you have a Master of Science or PhD degree in Electronics Engineering (in any field such as Embedded Systems and Multimedia, Electronics and Integrated Circuits, Nanotechnology, or other), Computer Engineering/Science, Physics, Applied Mathematics, or similar
- ▶ you're an analytic problem solver
- ▶ you're creative and eager to learn
- ▶ you're a team player
- ▶ you're fluent in English
- ▶ you're a fresh university master, or PhD graduate, or you have chip design experience in the industry for up to about five years

IT'S A PLUS IF:

- ▶ you have some experience designing embedded systems, digital ASICs, FPGAs, and/or some programming experience
- ▶ you have an understanding of mixed-signal or analog ASIC design

You will work at the easics office in Leuven - Belgium, conveniently located within five minutes walking distance of the Leuven railway station and city center.

Structural work from home is possible for up to two days per week.

From day one, you'll get extensive hands-on training by easics' experts, tailored to your field of study and your experience.

You will work on challenging state-of-the-art projects and have the option to grow into positions with increasing responsibilities such as senior SoC / ASIC design engineer, SoC / ASIC architect, and SoC / ASIC technical lead, and other positions.

easics is an equal opportunity employer. We value and recognize your talent with an attractive remuneration package.

Feel free to submit your application now. Please be aware that we will reach out to you once the hiring process kicks off again in March 2024.

SEND YOUR CV & MOTIVATION LETTER TO:
jobs@easics.com



easics is market leader in ASIC / SoC design & supply services, and FPGA / SoC-based designs. easics also develops and licenses nearAI, our semiconductor IP products for ultra-low power near-sensor AI.

End-markets include automotive, health-care, mobile, consumer, IoT, industrial, measurement equipment and space.

easics is a rock-solid company, in business for 30+ years and was founded in 1991. We are an independent company, ISO 9001:2015 certified, and headquartered in Leuven - Belgium.