

SoC / ASIC TECHNICAL LEAD

Location: LEUVEN – BELGIUM

easics is permanently looking for colleagues to surf the deep tech wave

You want to contribute on the frontline of the digital revolution?

Great!

You want to design state-of-the-art digital chips that will become the cornerstones of next-generation products in Ultra-Low Power mobile & consumer, healthcare, automotive, and other advanced applications such as smart Augmented Reality (AR) glasses?

Perfect!

You have a Master of Science or PhD degree in Electronics Engineering, Computer Engineering/Science, Physics, Applied Mathematics, or similar?

Terrific! Read on!

We make the world a smarter place

easics is a worldwide reference in smart electronic System-on-Chip (SoC) design. Our customers are world-leaders in their respective markets. We tackle some of the most advanced and rewarding challenges in first-of-a-kind digital & mixed-signal chips. Take for example: live, Ultra-Low Power pattern recognition using neural network inference at the extreme edge, top-notch hearing implants helping newborns and grandmas alike, a radiation-hardened camera for earth observation satellites, energy-harvesting wireless tags, ...

We discuss state-of-the-art algorithms and requirements with our customers and turn them into cutting-edge reality, tackling the challenges inherent to deep submicron implementations, all the way to 4-nanometer. We model these hardware challenges in software and optimize over all layers of abstraction. Trade-offs in power consumption, performance, and area are made along the way, resulting in the physical realization: a SoC or a custom-tailored ASIC at the heart of next-generation innovative products.

We'll make you an expert, no worries

Whether you are an eager starter or a seasoned professional, we'll guide you through our design methodology and introduce you to our vast body of expertise. easics is known to be a learning company; one that will give you the opportunity and time to become an expert, no matter what your previous experience is. You will join the easics academy from day one.

You will work on projects in diverse domains, making high-level models, mastering the intricacies of timing and parallelism, running tests in our lab or discussing requirements and architecture with the customer. You'll work in a highly autonomous team, where everyone is always ready to provide helping hands and brains to the others.

WE CAN'T WAIT TO SEE YOU JOIN OUR GROWING TEAM

See next page for more details



SoC / ASIC TECHNICAL LEAD FULL-TIME

Specializing in digital SoC, ASIC, FPGA, and DSP design

You will technically lead teams up to 8 SoC/ASIC design engineers or several smaller teams on different projects depending on your level of experience and personal interests. Together with your team(s) you will be responsible for creating complex digital chips. You will own and drive the whole design process, from specification, to validation and implementation, deciding on priorities together with the customer. During the project you are the primary technical contact for the customer and you will monitor the work in relation to the specification and architecture document and validation plans. This includes foreseeing or catching technical issues that could arise, and formulating a plan to tackle or avoid them. You guide and coach the easics team to ensure the work progresses smoothly, and people grow their skills as needed for the project.

DO APPLY IF:

- ▶ You have at least 5 years of broad technical experience in SoC/ASIC design from specification, to design and verification, to synthesis
- ▶ You are technically driven and still enjoy to do hands-on digital design from time to time for example when initiating a new project for a new client
- ▶ You are a leader with good coaching skills who loves taking his/her team to the next level
- ▶ You excel in professional communication and building long lasting relationships with the client
- ▶ You like to take ownership and do what it takes to bring your project(s) to a good end
- ▶ You are proactive and like to work in an entrepreneurial environment
- ▶ You're fluent in English

IT'S A PLUS IF:

- ▶ You have strong architectural skills and can set up complex architectures independently
- ▶ You have experience in RTL-to-GDS flows: place and route and sign-off flows
- ▶ You have experience in mixed-signal ASICs
- ▶ You have DSP experience

From day one, you will join a warm community of strong and passionate engineers.

easics foresees personal career guidance and aims to develop everyone's full potential.

In addition to working on state-of-the-art projects together with your team, you will get opportunities to contribute to easics' growth as an organisation depending on your personal interests and match with the organisation's vision and strategy.

You will work at the easics office in Leuven - Belgium, conveniently located within five minutes walking distance of the Leuven railway station and city center.

Structural work from home is possible for up to two days per week.

easics is an equal opportunity employer.

We value and recognize your talent with an attractive remuneration package.

APPLY TODAY

SEND YOUR CV & MOTIVATION LETTER TO:
jobs@easics.com



easics is market leader in ASIC / SoC design & supply services, and FPGA / SoC-based designs. easics also develops and licenses nearAI, our semiconductor IP products for ultra-low power near-sensor AI.

End-markets include automotive, health-care, mobile, consumer, IoT, industrial, measurement equipment and space.

easics is a rock-solid company, in business for 30+ years and was founded in 1991. We are an independent company, ISO 9001:2015 certified, and headquartered in Leuven - Belgium.