

# SUMMER JOBS & INTERNSHIPS

## Location: LEUVEN - BELGIUM

### Always looking for colleagues to ride the digital wave

You're interested in standing on the front line of digital evolution and want to be part of the newest improvements in the technological world?

**Great!**

You want to be part of the integration of digital designs in finished products?

**Perfect!**

You are studying for a Master of Science degree in Engineering Technology or similar?

**Terrific! Read on!**



## EASICS' RISC-V TURN-KEY SUB-SYSTEM

### DESCRIPTION:

#### Two RISC-V subsystems demonstrators to be built:

- ▶ 32-bit lightweight variant, with a few standard busses and bare-bone programming, up to a demo on FPGA, with communication to / from computer
- ▶ 64-bit high-performance variant, with bare-bone programming and Linux bootable variant, up to a demo on FPGA, and incorporating a software controlled display, using a framebuffer HDMI / DVI / VGA output IP from easics

### STUDENT PROFILE:

- ▶ you are an MSc student in Electronics Engineering, Computer Engineering/Science, Physics, Applied Mathematics, or similar
- ▶ you are a quick learner
- ▶ you have a good understanding of VHDL/Verilog
- ▶ you have a basic understanding of hardware design
- ▶ you have a keen interest in FPGA & digital ASIC design
- ▶ it is a plus if you have any experience with RISC-V

Duration: 4 to 8 weeks

Period: no specific constraints

**WE CAN'T WAIT TO SEE YOU JOIN OUR GROWING TEAM**

See next page for additional open positions



## MARKET RESEARCH ON SILICON COMPUTE FOR MOBILE / XR DEVICES

### DESCRIPTION:

For semiconductor chips in ultra-low power (ULP), always-on mobile / XR devices, at the Extreme Edge (ExE), close to the sensors and battery-powered:

- ▶ Artificial Intelligence (AI) / neural network inference applications roadmap and competitive landscape : pattern recognition, hands tracking, face recognition, Simultaneous Localization and Mapping (SLAM), etc. in smartphones, Extended Reality (XR), Augmented Reality (AR), Virtual Reality (VR) smart glasses, etc.
- ▶ RISC-V microprocessors applications roadmap

### STUDENT PROFILE:

- ▶ you are an MSc student in Electronics Engineering, Computer Engineering / Science, Commercial / Business Engineering, Physics, Applied Mathematics, or similar
- ▶ you have a keen business interest
- ▶ programming skills : basic hands-on Julia or Python experience is a plus, to automate graphic visualization

Duration: 5 or 6 weeks

Period: Within July / Aug / Sep 2023, but not more than 1 week in the period from 3 Aug till 23 Aug

## WEB APPLICATIONS FOR INTERACTIVE SCREENS

### DESCRIPTION:

- ▶ Make an interactive web application for a touch screen at the easics entrance, which will show announcements, birthday wishes, availability of the meeting rooms, etc. Additionally, make an application for meeting room screens, showing the availability of a specific meeting room, allowing to add a reservation, etc.
- ▶ You will collect data from different sources (e.g., google calendar, databases, ...) and make both a nice visualisation for touch screens, and an internally accessible user interface for configuration purposes.

### STUDENT PROFILE:

- ▶ you are an MSc student in Electronics Engineering, Computer Engineering/Science, Physics, Applied Mathematics, or similar
- ▶ you are a quick learner
- ▶ you are familiar with GNU/Linux
- ▶ you have a good understanding of Ruby On Rails
- ▶ knowledge of a javascript web framework is a plus

Duration: 6 weeks

Period: July / Aug / Sep 2023, but not more than 1 week in the period from 19 July till 16 Aug

 TO APPLY, SEND YOUR CV TO:  
**jobs@easics.com**



**easics**

A market leader in the embedded systems digital design by providing unique competence and development platforms that lead to first-time right, reliable and optimized logic and software that is maintainable by the customer. Supporting leading OEMs and semiconductor companies with custom designs and customizable IP blocks for these smart embedded processing systems that can be realized in both FPGAs and ASICs.