

FIELD APPLICATION ENGINEER

Location: LEUVEN - BELGIUM

easics is permanently looking for colleagues to surf the deep tech wave

You want to contribute on the frontline of the digital revolution?

Great!

You want to design state-of-the-art digital chips that will become the cornerstones of next-generation products in Ultra-Low Power mobile & consumer, healthcare, automotive, and other advanced applications such as smart Augmented Reality (AR) glasses?

Perfect!

You have a Master of Science or PhD degree in Electronics Engineering, Computer Engineering/Science, Physics, Applied Mathematics, or similar?

Terrific! Read on!

We make the world a smarter place

easics is a worldwide reference in smart electronic System-on-Chip (SoC) design. Our customers are world-leaders in their respective markets. We tackle some of the most advanced and rewarding challenges in first-of-a-kind digital & mixed-signal chips. Take for example: live, Ultra-Low Power pattern recognition using neural network inference at the extreme edge, top-notch hearing implants helping newborns and grandmas alike, a radiation-hardened camera for earth observation satellites, energy-harvesting wireless tags, ...

We discuss state-of-the-art algorithms and requirements with our customers and turn them into cutting-edge reality, tackling the challenges inherent to deep submicron implementations, all the way to 4-nanometer. We model these hardware challenges in software and optimize over all layers of abstraction. Trade-offs in power consumption, performance, and area are made along the way, resulting in the physical realization: a SoC or a custom-tailored ASIC at the heart of next-generation innovative products.

We'll make you an expert, no worries

Whether you are an eager starter or a seasoned professional, we'll guide you through our design methodology and introduce you to our vast body of expertise. easics is known to be a learning company; one that will give you the opportunity and time to become an expert, no matter what your previous experience is. You will join the easics academy from day one.

You will work on projects in diverse domains, making high-level models, mastering the intricacies of timing and parallelism, running tests in our lab or discussing requirements and architecture with the customer. You'll work in a highly autonomous team, where everyone is always ready to provide helping hands and brains to the others.

WE CAN'T WAIT TO SEE YOU JOIN OUR GROWING TEAM

See next page for more details



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FULL-TIME

Specializing in digital SoC, ASIC, FPGA, AI, and DSP

You will support our international customers to successfully integrate easics' state-of-the-art semiconductor Intellectual Property (IP) in their digital Systems-on-Chip (SoCs) and ASICs, for their game-changing applications. End markets include mobile, consumer, Internet-of-Things (IoT), healthcare, and automotive. And you will help them using easics' advanced software tools for Ultra-Low Power chip optimization and compilation. As their first point of contact you will also represent easics when prospects and customers request new features, enhancements or clarifications. In between these, you will design digital SoCs, ASICs, and/or FPGAs (using VHDL, (System)Verilog, UVM), develop software tools (using Python), and contribute to the specifications and documentation of easics' semiconductor IP portfolio.

DO APPLY IF:

- ▶ you have a keen interest in digital embedded systems and software methodology
- ▶ you have a Master of Science or PhD degree in Electronics Engineering (in any field such as Embedded Systems and Multimedia, Electronics and Integrated Circuits, Nanotechnology, or other), Computer Engineering/Science, Physics, Applied Mathematics, or similar
- ▶ you're an excellent communicator, both towards customers and internally at easics
- ▶ you are ready to travel internationally
- ▶ you are eager to learn
- ▶ you are a team player
- ▶ you are fluent in English
- ▶ you are a fresh university master, or PhD graduate, or you have chip design experience in the industry for up to about five years

IT'S A PLUS IF:

- ▶ you have some experience designing embedded systems, digital ASICs, FPGAs, and/or some programming experience
- ▶ you have an understanding of mixed-signal or analog ASIC design
- ▶ you have customer-facing experience in the semiconductor industry
- ▶ you speak German, Chinese, Korean, Japanese, and/or Hebrew

You will work at the easics office in Leuven - Belgium, conveniently located within five minutes walking distance of the Leuven railway station and city center. And you will travel to our international prospects and customers.

Structural work from home is possible for up to two days per week.

From day one, you'll get extensive hands-on training by easics' experts, tailored to your field of study and your experience.

You will work on challenging state-of-the-art projects and have the option to grow into positions with increasing responsibilities within the organization.

easics is an equal opportunity employer.

We value and recognize your talent with an attractive remuneration package.

APPLY TODAY

SEND YOUR CV & MOTIVATION LETTER TO:
jobs@easics.com



easics is market leader in digital SoC / ASIC design and supply services. easics licenses nearbAI™, its semiconductor Intellectual Property (IP) products for implementing Artificial Intelligence (AI) at the extreme edge, close to the image sensors. End-markets include mobile, consumer, Internet-of-Things (IoT), healthcare, automotive, industrial, and space. easics is a rock-solid company, in business for over 30 years. It was founded in 1991 in Leuven, as a spin-off company from KU Leuven and imec. Nowadays, easics is an independent company, headquartered in Leuven and with an office in Silicon Valley (California). easics is an ISO 9001:2015 certified company.